

# Ddr4 Sdram Registered Dimm Based On 4gb B Die

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### Ddr4 Sdram Registered Dimm Based

#### **288pin Registered DIMM based on 16Gb M-die**

Registered DIMM datasheet DDR4 SDRAM Rev 10 4 Registered DIMM PIN CONFIGURATIONS (FRONT SIDE/BACK SIDE) NOTE : 1) VPP is 25V DC 2) Pin 230 is defined as NC for UDIMMs, RDIMMs and LRDIMMs Pin 230 is defined as SAVE\_n for NVDIMMs 3) Pins 1 and 145 are defined as NC for UDIMMs, RDIMMs and L RDIMMs Pins 1 and 145 are defined as 12V for

#### **288pin Registered DIMM based on 8Gb C-die**

datasheet DDR4 SDRAM Rev 15 Registered DIMM 4 REGISTERED DIMM PIN CONFIGURATIONS (FRONT SIDE/BACK SIDE) NOTE : 1) VPP is 25V DC 2) Pin 230 is defined as NC for UDIMMs, RDIMMs and LRDIMMs Pin 230 is defined as SAVE\_n for NVDIMMs 3) Pins 1 and 145 are defined as NC for UDIMMs, RDIMMs and L RDIMMs Pins 1 and 145 are defined as 12V for

#### **DDR4 SDRAM Registered DIMM Based on 4Gb A-die**

288pin DDR4 SDRAM Registered DIMM \*SK hynix reserves the right to change products or specifications without notice DDR4 SDRAM Registered DIMM Based on 4Gb A-die HMA451R7AFR8N HMA41GR7AFR8N HMA41GR7AFR4N HMA42GR7AFR4N

#### **DDR4 SDRAM Registered DIMM Based on 8Gb C-die**

SK hynix Registered DDR4 SDRAM DIMMs (Registered Double Data Rate Synchronous DRAM Dual In-Line Memory Modules) are low power, high-speed operation memory modules that use DDR4 SDRAM devices These Registered SDRAM DIMMs are intended for use as main memory when installed in systems such as servers and workstations

#### **288pin Registered DIMM based on 8Gb B-die**

Registered DIMM datasheet DDR4 SDRAM Rev 191 4 Registered DIMM Pin Configurations (Front side/Back side) NOTE: 1 VPP is 25V DC 2 Pin 230 is defined as NC for UDIMMs, RDIMMs and LRDIMMs Pin 230 is defined as SAVE\_n for NVDIMMs 3 Pins 1 and 145 are defined as NC for UDIMMs, RDIMMs and LRDI MMs Pins 1 and 145 are defined as 12V for

**288pin Registered DIMM based on 8Gb C-die**

Registered DIMM datasheet DDR4 SDRAM Rev 13 4 REGISTERED DIMM PIN CONFIGURATIONS (FRONT SIDE/BACK SIDE) NOTE : 1) VPP is 25V DC 2) Pin 230 is defined as NC for UDIMMs, RDIMMs and LRDIMMs Pin 230 is defined as SAVE\_n for NVDIMMs 3) Pins 1 and 145 are defined as NC for UDIMMs, RDIMMs and L RDIMMs Pins 1 and 145 are defined as 12V for

**DDR4 SDRAM Unbuffered DIMM Based on 8Gb M-die**

288pin DDR4 SDRAM Unbuffered DIMM \*SK hynix reserves the right to change products or specifications without notice DDR4 SDRAM Unbuffered DIMM Based on 8Gb M-die HMA81GU6MFR8N HMA81GU7MFR8N HMA82GU6MFR8N HMA82GU7MFR8N

**DDR4 SDRAM Unbuffered DIMM Based on 8Gb C-die**

288pin DDR4 SDRAM Unbuffered DIMM DDR4 SDRAM Unbuffered DIMM Based on 8Gb C-die HMA851U6CJR6N HMA81GU6CJR8N HMA81GU7CJR8N HMA82GU6CJR8N On-Die Termination: ODT (registered HIGH) enables RTT\_NOM termination resistance internal to the DDR4 SDRAM When enabled, ODT is only applied to each DQ, DQS\_t,

**DDR4 SDRAM Unbuffered DIMM Based on 4Gb M-die**

288pin DDR4 SDRAM Unbuffered DIMM DDR4 SDRAM Unbuffered DIMM Based on 4Gb M-die HMA451U6MFR8N HMA41GU6MFR8N Rev 13 / Dec2015 2 Revision History On-Die Termination: ODT (registered HIGH) enables RTT\_NOM termination resistance internal to the DDR4 SDRAM When enabled, ODT is only applied to each DQ, DQS\_t,

**DDR4 SDRAM NVRDIMM - micron.com**

DDR4 SDRAM NVRDIMM MTA36ASS4G72XF1Z - 32GB Features • Nonvolatile registered DIMM (NVRDIMM) - Highly reliable nonvolatile memory solution - DDR4 RDIMM, NAND Flash, and PowerGEM management integrated in single module - Persistent energy source options • Option 1: Battery-free power source (Power-GEM) • Option 2: Persistent DDR4 12V pin

**288pin VLP Registered DIMM based on 8Gb B-die**

- 6 - datasheet DDR4 SDRAM Rev 111 VLP Registered DIMM 5 Pin Description NOTE : 1 Address A17 is only valid for 16 Gb x4 based SDRAMs 2 RAS\_n is a multiplexed function with A16

**DDR4 - Transcend Information**

DDR4 TS1GHR72V1Z TS2GHR72V1Z 288Pin DDR4 2133 RDIMM 8GB~16GB Based on 1Gx4 CAS\_n Description DDR4 Registered DIMM is high-speed, low power memory module that use 1Gx4bits DDR4 SDRAM in FBGA package and a 4Kbits serial EEPROM on a 288-pin printed circuit board DDR4 Registered DIMM is a

**DDR4 LRDIMM Memory Model - Cadence IP**

DDR4 LRDIMM Memory Model VIP supports JEDEC DDR4 SDRAM Registered DIMM Design Specification(Rev 08) for RDIMM specification and follows JEDEC DDR4 Register - DDR4RCD01 - Rev 092 for the RCD specification Key Features • Support for signal strength modeling Users can use pull up or pull down on the inout pins and the model

**DDR4 8Gb B die Registered DIMM Rev1.8 Oct.16**

datasheet DDR4 SDRAM Rev 18 Registered DIMM 4 Registered DIMM Pin Configurations (Front side/Back side) NOTE: 1 VPP is 25V DC 2 Pin 230 is defined as NC for UDIMMs, RDIMMs and LRDIMMs Pin 230 is defined as SAVE\_n for NVDIMMs 3 Pins 1 and 145 are defined as NC for UDIMMs, RDIMMs and LRDI MMs Pins 1 and 145 are defined as 12V for Hybrid

**DDR3 RDIMM based on 4Gb - Advantech**

288Pin DDR4 2133 ECC UDIMM 4GB Based on 512Mx8 AQD-D4U4GR21-SG 2 Description DDR4 Registered DIMM is high-speed, low power memory module that use 512Mx8bits DDR4 SDRAM in FBGA package and a 4Kbits serial EEPROM on a 288-pin printed circuit board DDR4 Registered DIMM is a Dual In-Line Memory Module and is intended for mounting into

**Memory Module Specifications - Kingston Technology**

DDR4-2133 CL15 SDRAM (Synchronous DRAM) registered w/ parity, 2Rx4, ECC, memory module, based on thirty-six 1G x 4-bit FBGA components The SPD is programmed to JEDEC standard latency DDR4-2133 timing of 15-15-15 at 12V Each 288-pin DIMM uses gold contact fingers The electrical and mechanical specifications are as follows: CL(IDD)

**288pin Unbuffered DIMM based on 8Gb B-die**

- 5 - datasheet DDR4 SDRAM Rev 20 Unbuffered DIMM 4 Pin Description NOTE : 1 Address A17 is not valid for x8 and x16 based SDRAMs For UDIMMs this connection pin is NC

**DDR4 SDRAM Registered DIMM - Super Talent Technology**

DDR4 SDRAM Registered DIMM 8GB based on 4Gbit (512Mx8) component Revision 10 (SEPT, 2014) -Initial Release The pin description table below is a comprehensive list of all possible pins for all DDR4 modules Pins listed below may not be all supported on this module Please see Pin Assignments for information specific to this module

**External Memory Interface Handbook Volume 2: Design ...**

External Memory Interface Handbook Volume 2: Design Guidelines [Send Feedback](#) [Send Feedback](#)

**DDR3 RDIMM based on 4Gb - Advantech**

288Pin DDR4 2133 RDIMM 16GB Based on 1Gx8 AQD-D4U16R21-SE 2 Description DDR4 Registered DIMM is high-speed, low power memory module that use 1Gx8bits DDR4 SDRAM in FBGA package and a 4Kbits serial EEPROM on a 288-pin printed circuit board DDR4 Registered DIMM is a Dual In-Line Memory Module and is intended for mounting into